

Type	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
1	BRS 1	(random adj early adj detection) and (switch\$3 near3 fabric) and ((queue\$3 or buffer) near threshold)	USPAT; US-PGPUB; EPO; JPO	2003/11/25 14:17		
2	BRS 23	(random adj early adj detection) and (switch\$3 near3 fabric) and ((queue\$3 or buffer) near3	USPAT; US-PGPUB; EPO; JPO	2003/11/24 18:57		
3	BRS 1	6333917.pn. and card	USPAT; US-PGPUB; EPO; JPO	2003/11/24 17:03		
4	BRS 0	6333917.pn. and (segment\$3 or convert\$3)	USPAT; US-PGPUB; EPO; JPO	2003/11/24 13:59		
5	BRS 1	6333917.pn. and (count\$3 or calculat\$3 or ratio or chip or microprocessor or asic or priority)	USPAT; US-PGPUB; EPO; JPO	2003/11/24 14:10		
6	BRS 2	6333917.URPN.	USPAT	2003/11/24 14:03		
7	BRS 9	("4769810" "5426640" "5650993" "5689500" "5864539" "6069872" "6141323" "6144636" "6157613").PN.	USPAT	2003/11/24 14:03		
8	BRS 0	6333917.pn. and coputer	USPAT; US-PGPUB; EPO; JPO	2003/11/24 14:10		
9	BRS 0	6333917.pn. and computer	USPAT; US-PGPUB; EPO; JPO	2003/11/24 14:11		
10	BRS 166	computer and (linecard\$1 or (line adj card\$1)) and fabric and ip and convert\$3	USPAT; US-PGPUB; EPO; JPO	2003/11/24 14:12		

Type	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
11	BRS 63	((computer and (linecard\$1 or (line adj card\$1)) and fabric and ip and convert\$3) and red	USPAT; US-PGPUB; EPO; JPO	2003/11/24 14:18		
12	BRS 103	((computer and (linecard\$1 or (line adj card\$1)) and fabric and ip and convert\$3) not ((computer and (linecard\$1 or (line adj card\$1)) and fabric and ip and convert\$3) and red)	USPAT; US-PGPUB; EPO; JPO	2003/11/24 14:18		
13	BRS 1	6333917.pn. and discard\$3	USPAT; US-PGPUB; EPO; JPO	2003/11/24 15:20		
14	BRS 0	(random adj early adj detection) same computer same network	USPAT; US-PGPUB; EPO; JPO	2003/11/24 16:08		
15	BRS 4	(random adj early adj detection) same computer same network	USPAT; US-PGPUB; EPO; JPO	2003/11/24 16:15		
16	BRS 17	(random adj early adj detection) and computer and chip and ASIC	USPAT; US-PGPUB; EPO; JPO	2003/11/24 16:17		
17	BRS 39	((random adj early adj detection) and computer and (chip or ASIC))	USPAT; US-PGPUB; EPO; JPO	2003/11/24 16:17		
18	BRS 22	((random adj early adj detection) and computer and (chip or ASIC)) not ((random adj early adj detection) and computer and chip	USPAT; US-PGPUB; EPO; JPO	2003/11/24 16:18		

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19	BRS 1	6333917.pn. and (priority or class)	USPAT; US-PGPUB; EPO; JPO	2003/11/24 18:29		
20	BRS 0	6333917.pn. and (software or readable)	USPAT; US-PGPUB; EPO; JPO	2003/11/24 17:09		
21	BRS 0	6333917.pn. and (electromagnetic)	USPAT; US-PGPUB; EPO; JPO	2003/11/24 17:09		
22	BRS 1	6333917.pn. and (signal)	USPAT; US-PGPUB; EPO; JPO	2003/11/24 17:10		
23	BRS 1	6333917.pn. and hardware	USPAT; US-PGPUB; EPO; JPO	2003/11/24 17:11		
24	BRS 1	(6333917.pn. and hardware) and (process\$3 or micro)	USPAT; US-PGPUB; EPO; JPO	2003/11/24 17:16		
25	BRS 12	(linecard or "line card") same micro\$1processor same chip	USPAT; US-PGPUB; EPO; JPO	2003/11/24 17:17		
26	BRS 1	6333917.pn. and total	USPAT; US-PGPUB; EPO; JPO	2003/11/24 18:29		
27	BRS 39	(random adj early adj detection) and (switch\$3 near3 fabric) and (queue\$3 or buffer)	USPAT; US-PGPUB; EPO; JPO	2003/11/24 18:57		